



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#15/E
2/20/03
Surber

Applicant: Yasuyuki MORISHITA
Title: SEMICONDUCTOR DEVICE
Application No.: 09/621,614
Filing Date: 07/21/00
Examiner: Nadav, Ori
Art Unit: 2811

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TECHNOLOGY CENTER-2800

REPLY TO OFFICIAL ACTION OF 5 NOVEMBER 2002
UNDER 37 CFR §1.111

Commissioner for Patents
Box Non-Fee Amendment
Washington, D.C. 20231

Sir:

Applicant hereby replies to the official action mailed 5 November 2002.
Reconsideration in view of the following amendments and remarks is requested.

AMENDMENTS

In the claims:

The following shows the present status of all pending claims:

1. (Twice Amended) A semiconductor device having an input/output protection circuit section on a semiconductor substrate, the input/output protection circuit section comprising:
- a plurality of field effect transistors connected in parallel, each of which has a source diffusion layer of a first conductive type and a drain diffusion layer